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NOTIFICATION OF TRANSMITTAL
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OF THE INTERNATIONAL PRELIMINARY REPORT
ON PATENTABILITY
(CHAPTER I OR CHAPTER II
OF THE PATENT COOPERATION TREATY)

(PCT Rule 72.2)

To:

MIYAZAKI, Teruo 8th Floor, 16th Kowa Bldg. 9-20, Akasaka 1-chome Minato-ku, Tokyo 107-0052 JAPON



Date of mailing (day/month/year) 04 August 2005 (04.08.2005)	PATENT
Applicant's or agent's file reference NEC03P228	IMPORTANT NOTIFICATION
International application No. PCT/JP2003/015652	International filing date (day/month/year) 08 December 2003 (08.12.2003)

Applicant

NEC CORPORATION et al

1. Transmittal of the translation to the applicant.

The International Bureau transmits herewith a copy of the English translation made by the International Bureau of the international preliminary examination report established by the International Preliminary Examining Authority.

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It is the applicant's responsibility to prepare and furnish such translation directly to each elected Office concerned (Rule 74.1). See Volume II of the PCT Applicant's Guide for further details.

The International Bureau of WIPO 34, chemin des Colombettes 1211 Geneva 20, Switzerland Authorized officer

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Translation

PATENT COOPERATION TREATY



PCT

INTERNATIONAL PRELIMINARY EXAMINATION REPORT

(PCT Article 36 and Rule 70)

Applicant's or agent's file reference							
NEC03P228	FOR FURTHER ACTION See Notification of Transmittal of International Preliminary Examination Report (Form PCT/IPEA/416)						
International application No.	International filing date (day)	Priority date (day/month/year)					
PCT/JP2003/015652	08 December 2003 (08	3.12.2003)	06 December 2002 (06.12.2002)				
International Patent Classification (IPC) or n H04N 5/232	national classification and IPC						
Applicant	NEC CORPORA	TION					
This international preliminary exam and is transmitted to the applicant ac	ination report has been prepare coording to Article 36.	d by this Interr	national Preliminary Examining Authority				
2. This REPORT consists of a total of	4 sheets, includ	ing this cover s	heet.				
amended and are the basis for	ted by ANNEXES, i.e., sheets or this report and/or sheets contain Administrative Instructions un	ining rectifica	on, claims and/or drawings which have been tions made before this Authority (see Rule				
These annexes consist of a to	tal of sheets.						
3. This report contains indications rela	3. This report contains indications relating to the following items:						
I Basis of the report							
II Priority							
III Non-establishment of opinion with regard to novelty, inventive step and industrial applicability							
IV Lack of unity of invention							
Neasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement							
VI Certain documents cited							
VII Certain defects in the international application							
VIII Certain observations on the international application							
Date of submission of the demand Date of completion of this report							
		•					
08 December 2003 (08.12.2003) 28 July 2004 (28.07.2004)							
Name and mailing address of the IPEA/JP	rized officer						
Facsimile No.	Teleph	ione No.					

Form PCT/IPEA/409 (cover sheet) (July 1998)



INTERNATIONAL PRELIMINARY EXAMINATION REPORT

International application No.

PCT/JP2003/015652

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· [_	J	th	ndments have resulted in the cancellation e description, pages e claims, Nos e drawings, sheets/fig		
. [Thi bey	is repo yond th	t has been established as if (some of) the disclosure as filed, as indicated in the s	he amendments had not been made, sinc Supplemental Box (Rule 70.2(c)).**	e they have been considered to go
and	170.17	7).	s originally flied and are not anno	eceiving Office in response to an invitation exed to this report since they do not	contain amendments (Rule 70.16
	, . op		- sinces containing such amenaments mus	st be referred to under item 1 and annexe	d to this report.



International application No.

		INATION REPORT	PC1/JP03/	15652		
. Reasoned statement under Article 35(2) with regard to novelty, inventive step or industrial applicability; citations and explanations supporting such statement						
Statement	or and sacin statement					
Novelty (N)	Claims	1-7		YES		
	Claims			NO		
Inventive step (IS)	Claims	1-7		YES		
	Claims			NO NO		
Industrial applicability (IA)	Claims	1-7		YES		
	Claims			NO		
Citations and explanations						
would they be obvious to	a party skilled i	n the art.				
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International application No.

PCT/JP03/15652

VIII. Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully supported by the description, are made:

According to the written amendment of January 7, 2004, the specification (page 13, lines 19-21) was amended to "buffered in internal RAM 75 to be used for compressing/encoding image data after image processing as image data D_{p3} and supplied to RSPB 76," and a missing arrow was added to Fig. 4.

However, the above amended description contradicts both the description of the specification (page 13, lines 18-19 and lines 21-26) and the description of amended Fig. 4.

In other words, image data after image processing in a regulator circuit 72 are not image data \underline{D}_{p3} but image data \underline{D}_{p1} . According to the description of the specification (page 13, lines 22-26), image data \underline{D}_{p1} after image processing are not buffered to the internal RAM 75 as they are but the image data \underline{D}_{p1} should be resize processed by the RSPB 76 and buffered to the internal RAM 75 as image data \underline{D}_{p3} .

Regarding Fig. 4 as well, an arrow does not direct from the internal RAM 75 to the RSPB 76 but directs from the RSPB 76 to the internal RAM 75.

Further, in Fig. 4, a data flow of a hook-shaped line connected from a line linking the regulator circuit 72 and RSPB 76 to a line linking an external RAM controller 73 and the internal RAM 75 is still not clear, and which data are to be outputted from where to where are not clear.

In other words, if image data D_{p1} are to be outputted from the regulator circuit 72 to the external RAM controller 73, a hook should face an opposite direction. Also, if image data D_{p3} are to be outputted from the RSPB 76 to the external RAM controller 73, it is unnatural because image data D_{p1} and image data D_{p3} exist on the same line. As image data D_{p3} are buffered in the internal RAM 75 from the beginning, the above hook-shape line is considered unnecessary.